



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Schroeder, et al. Attorney Docket: INF-128
Filed: January 22, 2004 Examiner: TBD
Serial No.: 10/761,242 Art Unit: 2818
For: DRAM Memory with a Shared Sense Amplifier Structure

Mail Stop: Amendment
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

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Applicants wish to bring to the attention of the Patent and Trademark Office the information noted on the enclosed form PTO/SB/08a that may be considered material to the examination of the above-identified application.

No fee is due at this time, as this Information Disclosure Statement is being filed pursuant to 37 C.F.R. § 1.97(b)(3), before the mailing of a first Office action on the merits.

Respectfully submitted,

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October 20, 2004

Date

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Complete if Known

Application Number	10/761,242
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Filing Date	1/22/2004
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First Named Inventor	Schroeder, et al.
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Art Unit	2818
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Examiner Name	TBD
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Attorney Docket Number | INF-128

Sheet

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U.S. PATENT DOCUMENTS

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FOREIGN PATENT DOCUMENTS

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Examiner Signature		Date Considered	
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